ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

Memory Module with Dynamic Termination Using Bus Switches
Timed by Memory Clock and Chip Select

Submision Type: Utility Patent Filing

Application Number:

10/710475

EFS ID: 64505

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First Named Applicant: Yao Yen
Attorney Docket Number: PS-122

Timestamp: 2004-07-14 12:00:56 EDT

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File Listing:

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PS122ex-usfees.xml		
us-fee-sheet.xsl	24912	2004-07-14
us-fee-sheet.dtd	11069	2004-07-14
PS122ex-usidst.xml	2938	2004-07-14
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us-ids.xsl	12026	2004-07 - 14
PS122ex-usrequ.xml	2184	2004-07-14
us-request.dtd	19064	2004-07-14
us-request.xsl	33300	2004-07-14
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PS122e-trans.xml	44256	2004-07-14
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application-body.dtd	49498	2004-07-14
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mathml2.dtd	54588	2004-07-14
mathml2-qname-1.mod	13225	2004-07-14
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isoamso.ent	1934	2004-07-14
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